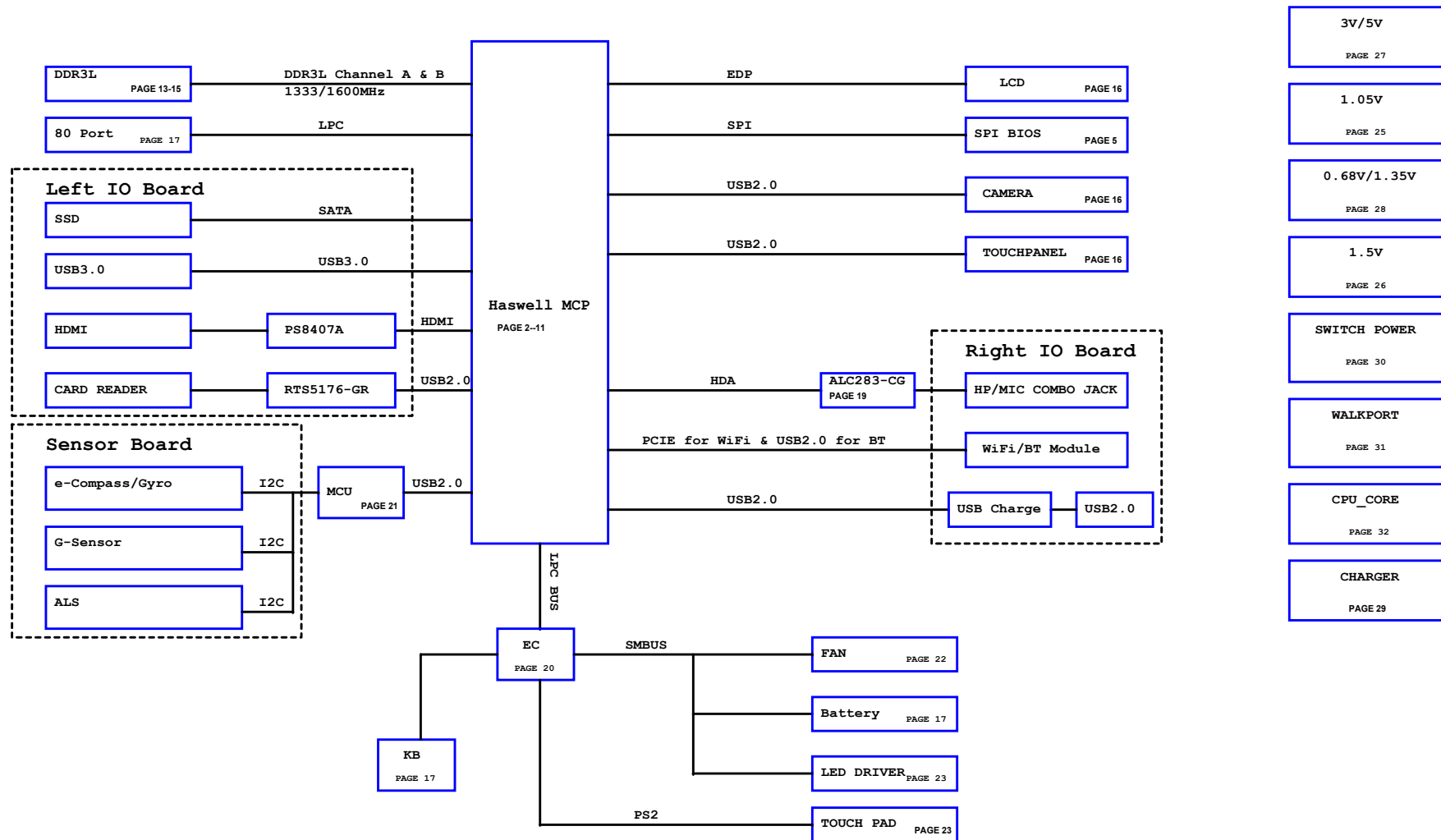
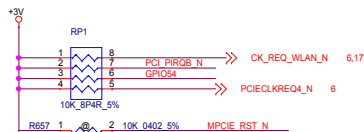
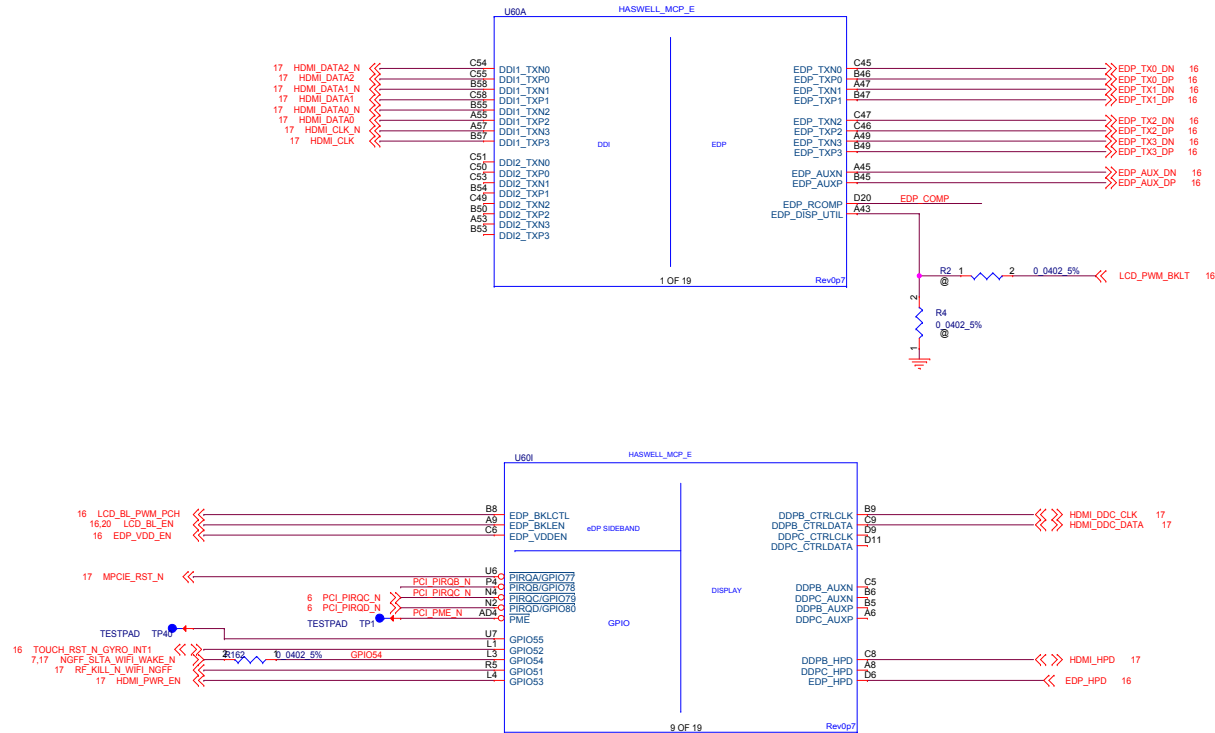


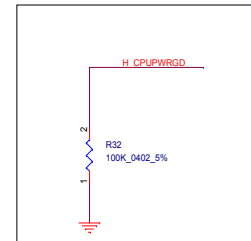
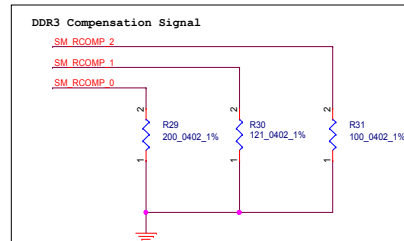
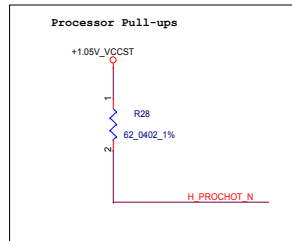
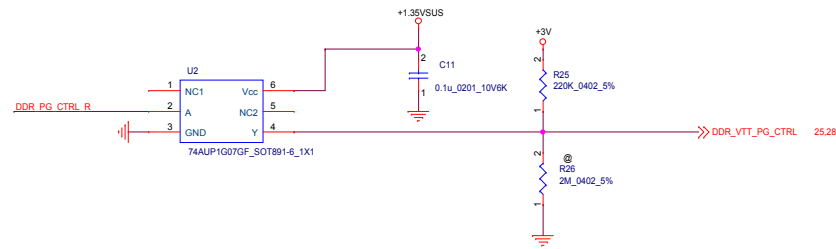
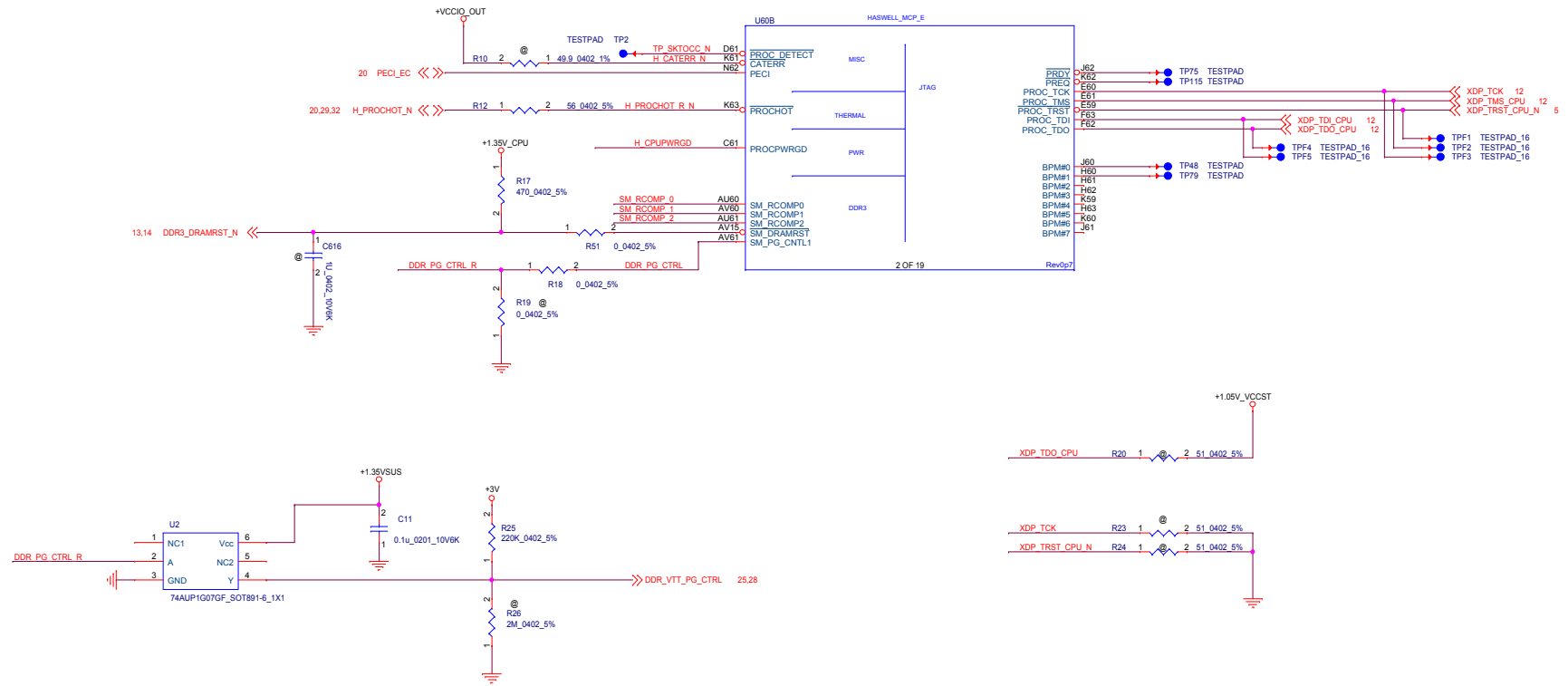
Kona SVT-NCS Schematic Block Diagram



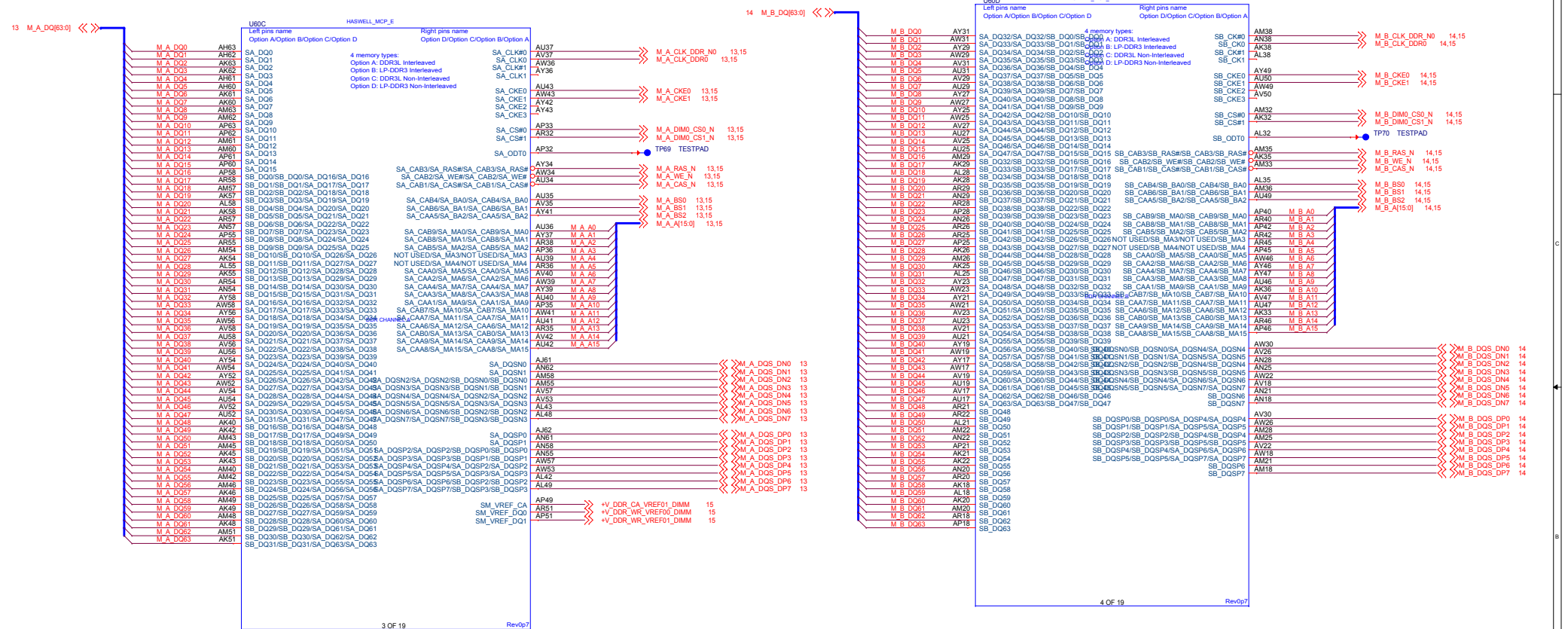
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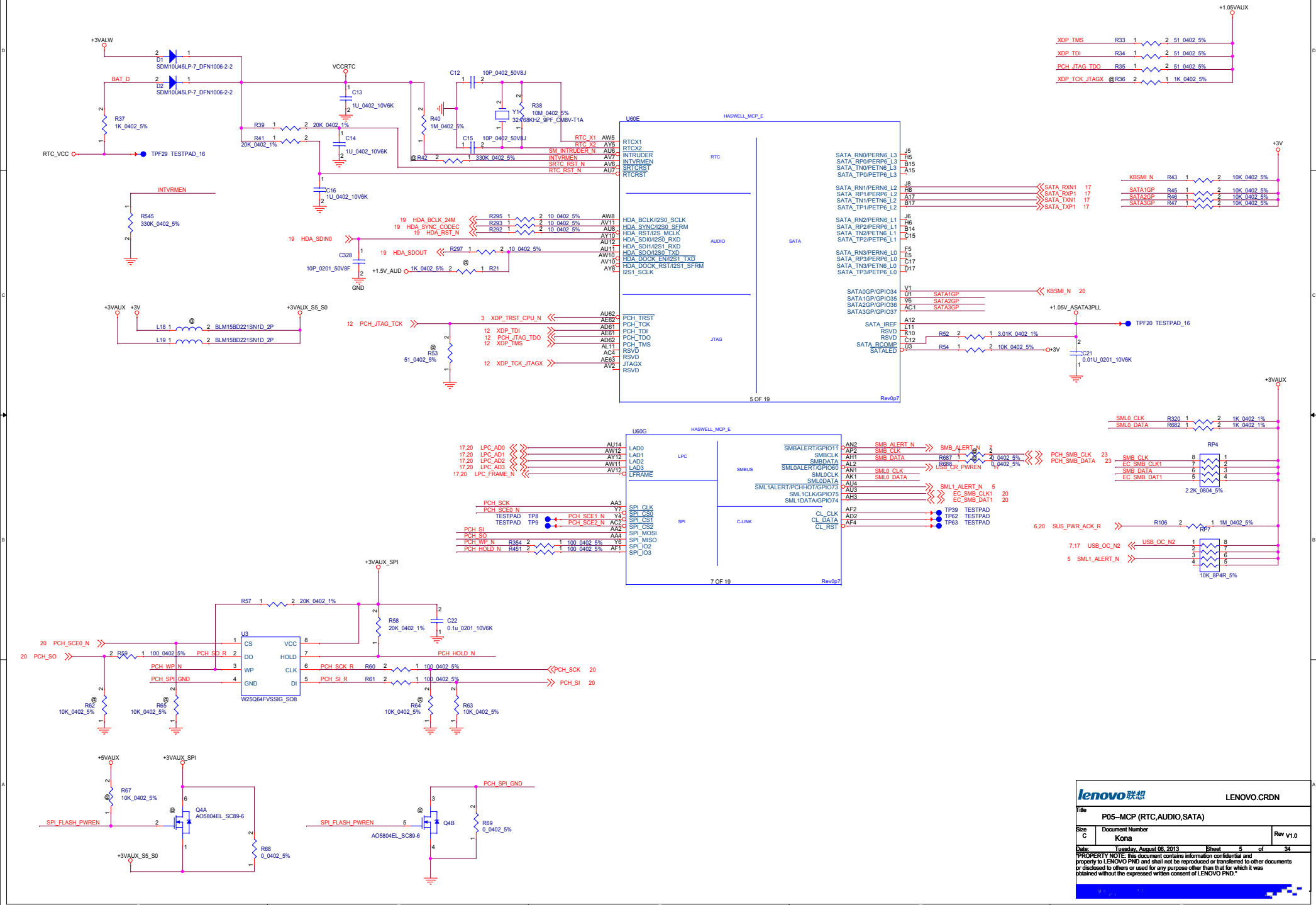
Haswell MCP (MISC,THERMAL,JATG)



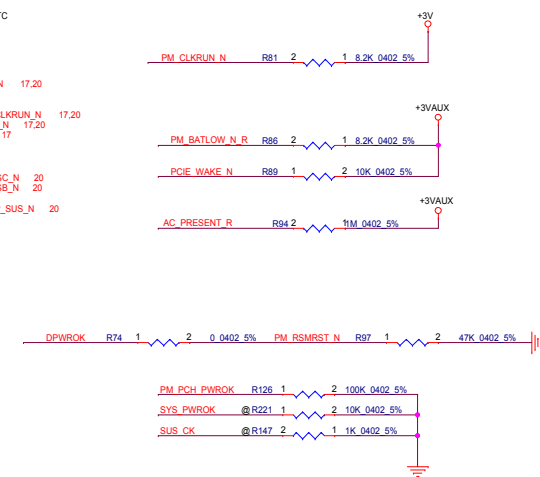
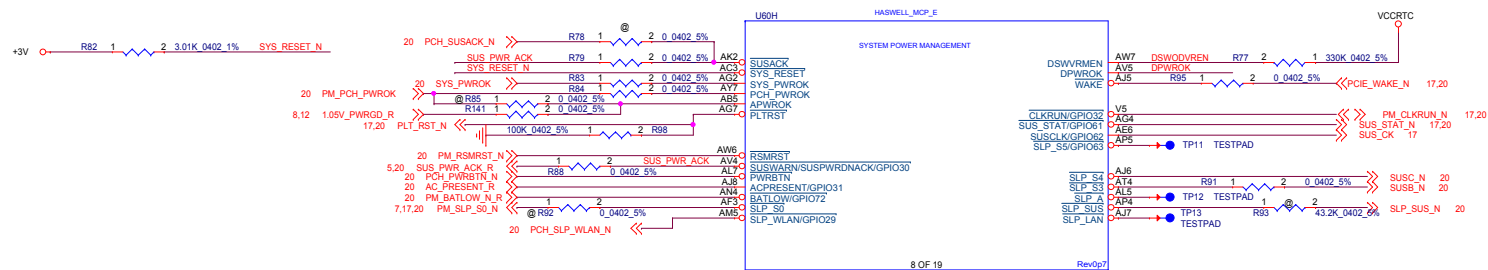
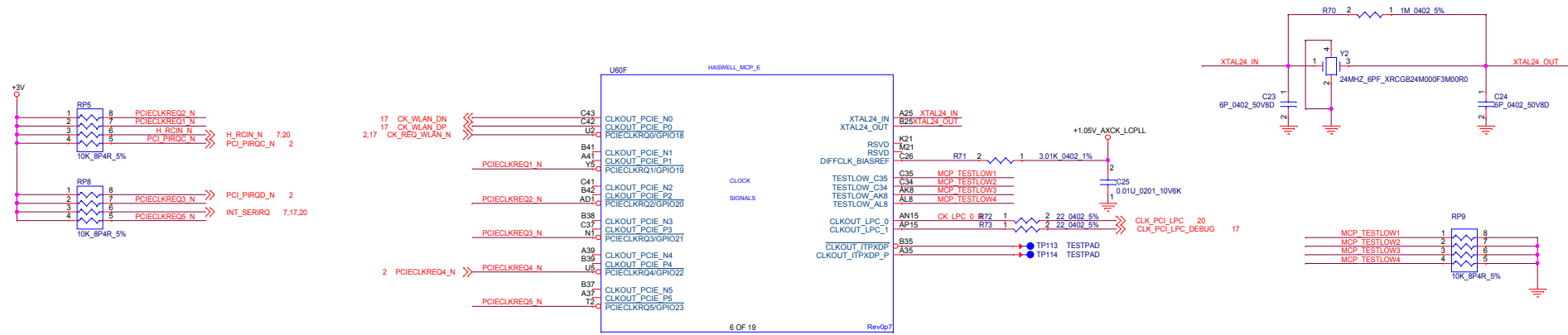
Haswell MCP (LPDDR3)



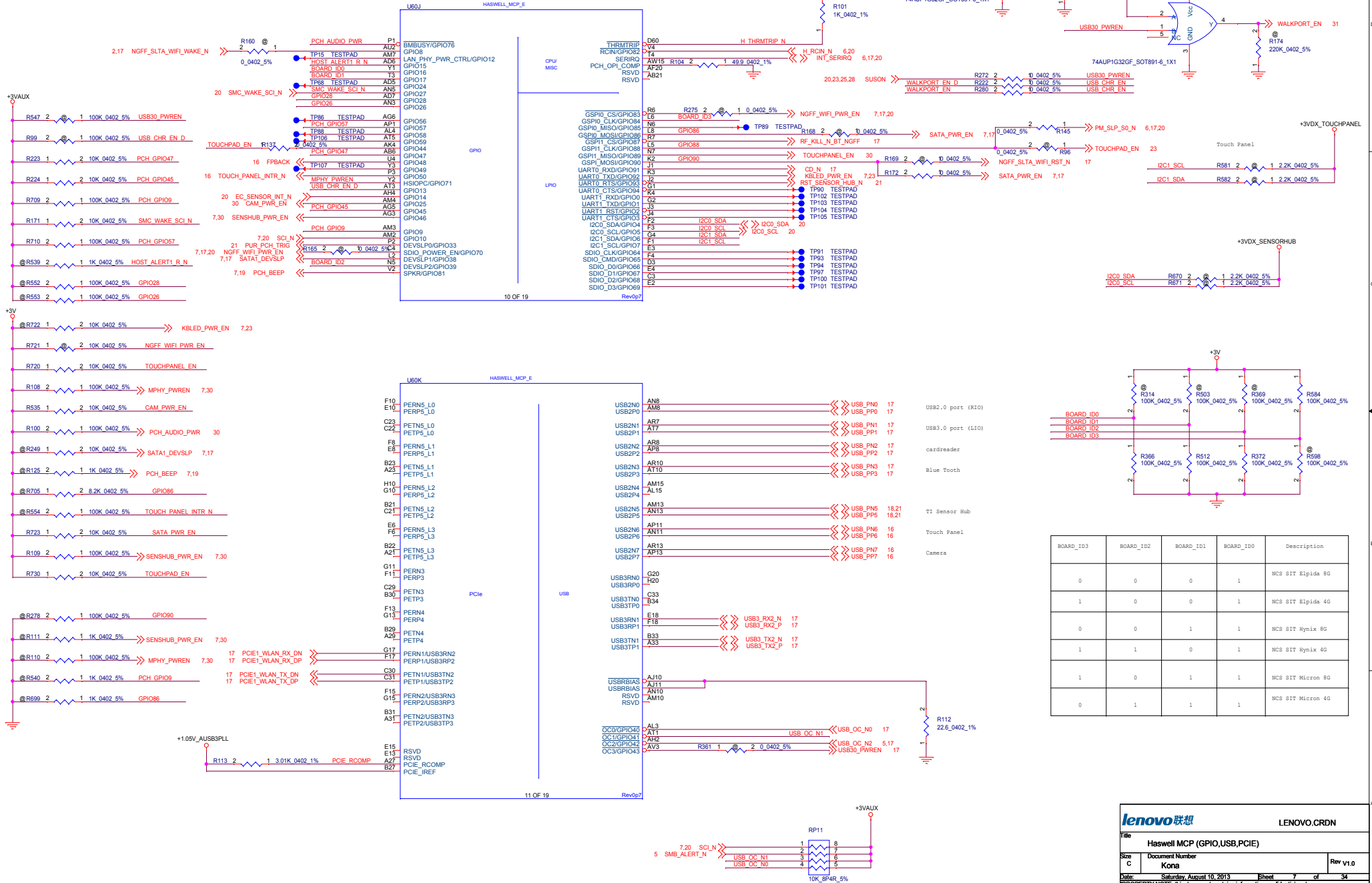
Haswell MCP (RTC,AUDIO,JTAG,SATA,LPC,SPI,SMBUS)



Haswell MCP (Clock, PM)

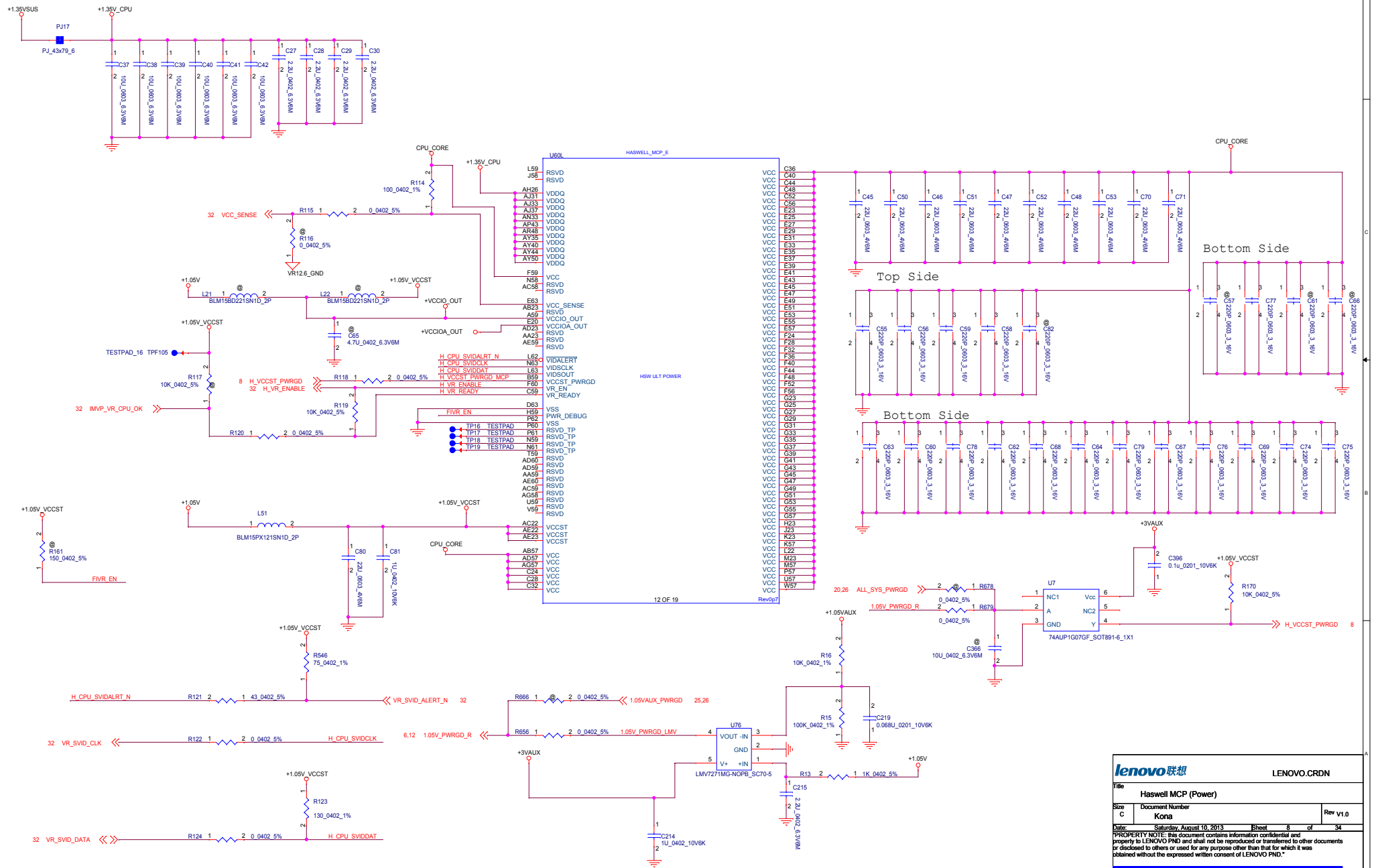


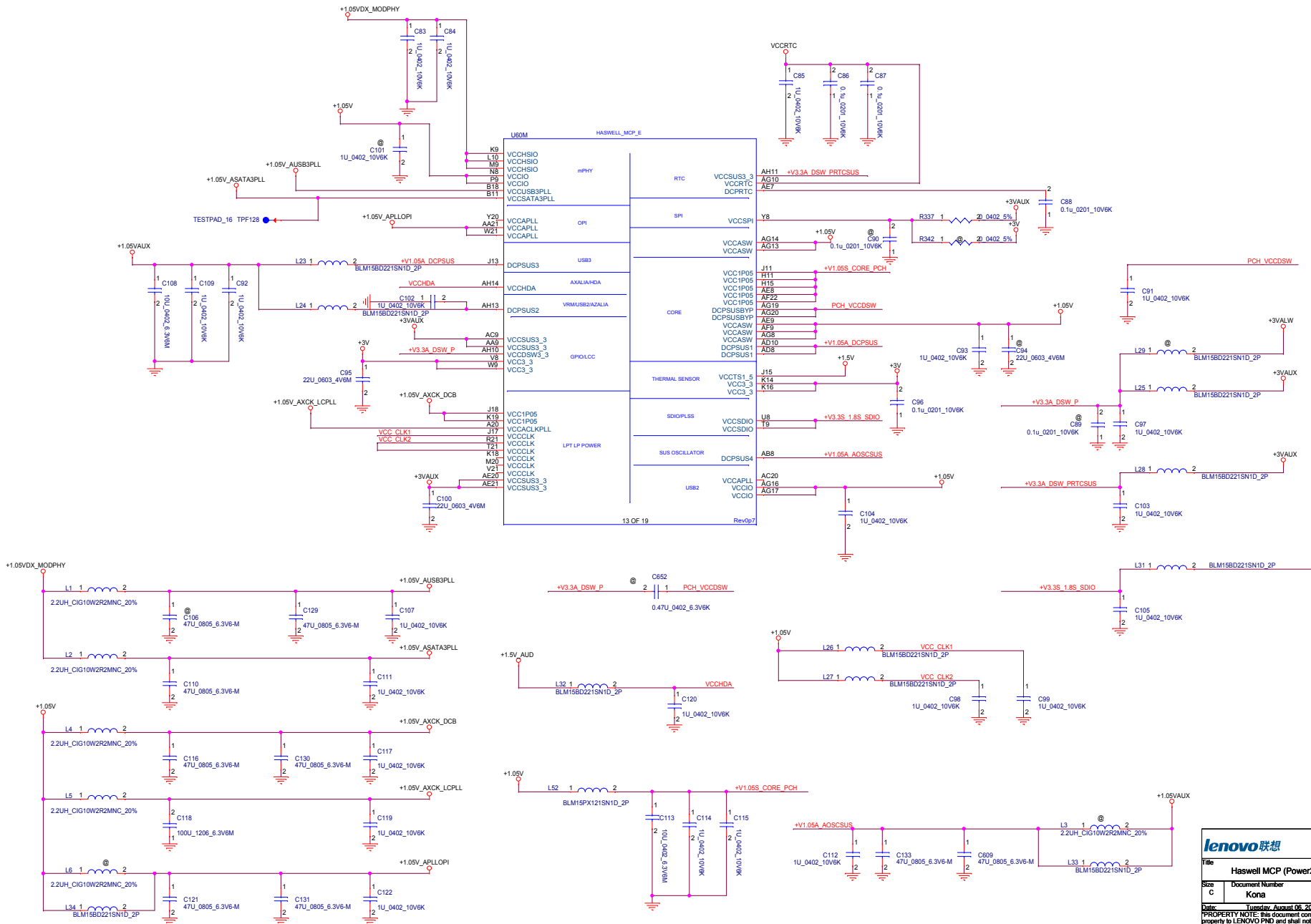
Haswell MCP (GPIO,USB,PCIE)



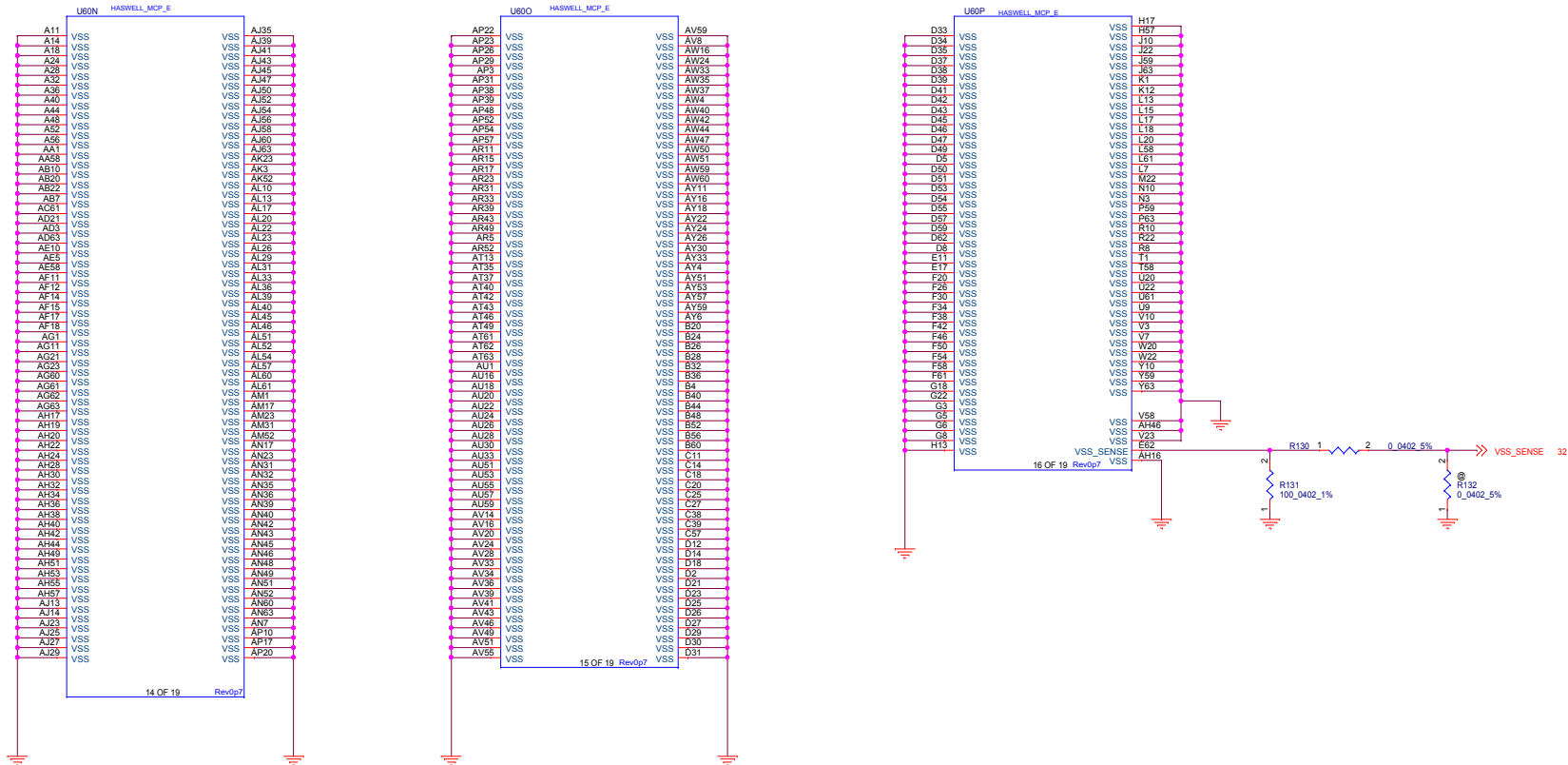
BOARD_ID3	BOARD_ID2	BOARD_ID1	BOARD_ID0	Description
0	0	0	1	NCS SIT Elpida 8G
1	0	0	1	NCS SIT Elpida 4G
0	0	1	1	NCS SIT Hynix 8G
1	1	0	1	NCS SIT Hynix 4G
1	0	1	1	NCS SIT Micron 8G
0	1	1	1	NCS SIT Micron 4G

Haswell MCP (Power)

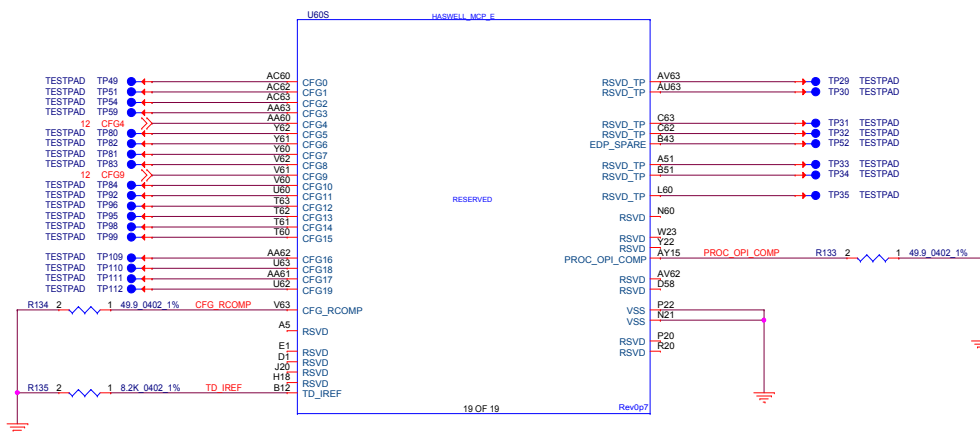
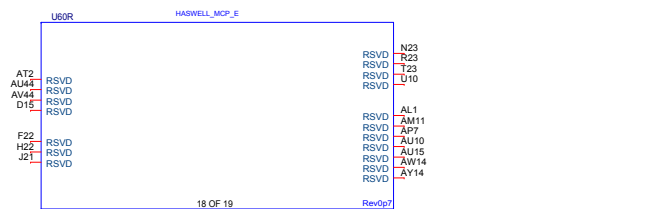
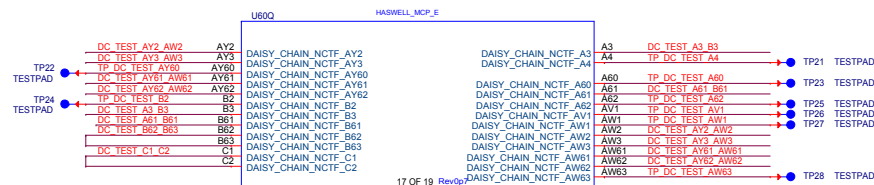


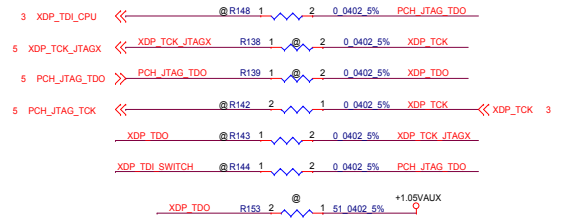
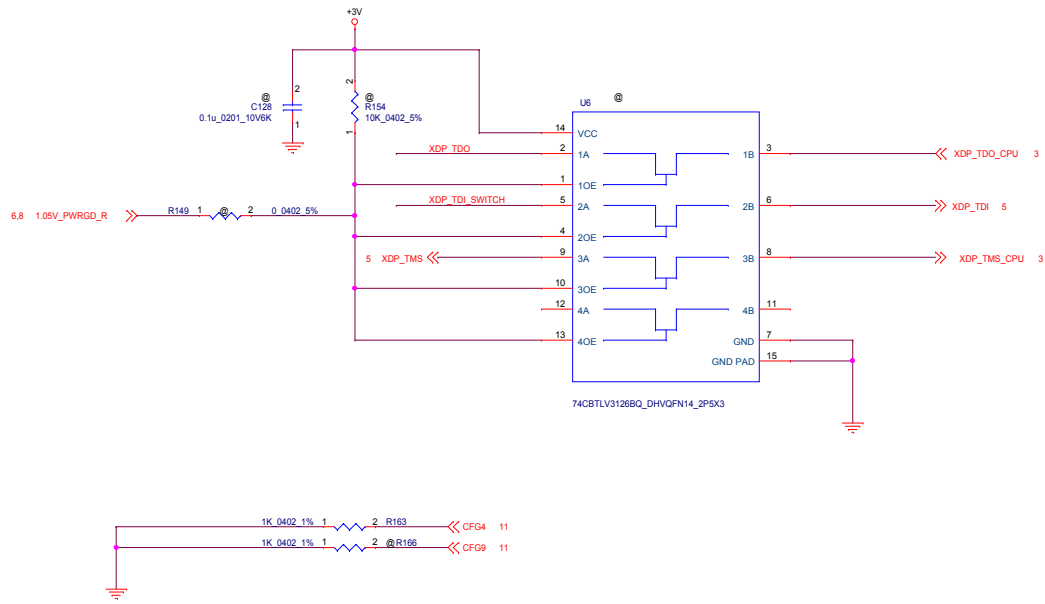
Haswell MCP (Power2)

Haswell MCP (VSS)



Haswell MCP (OTHER)





D

C

B

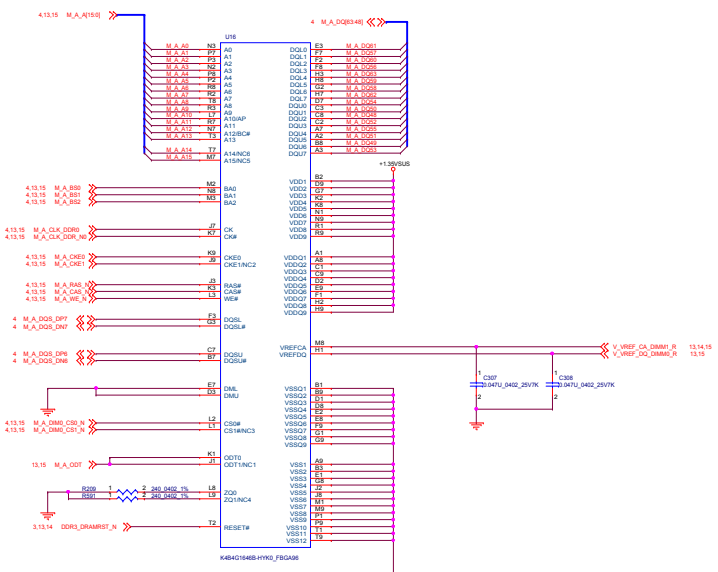
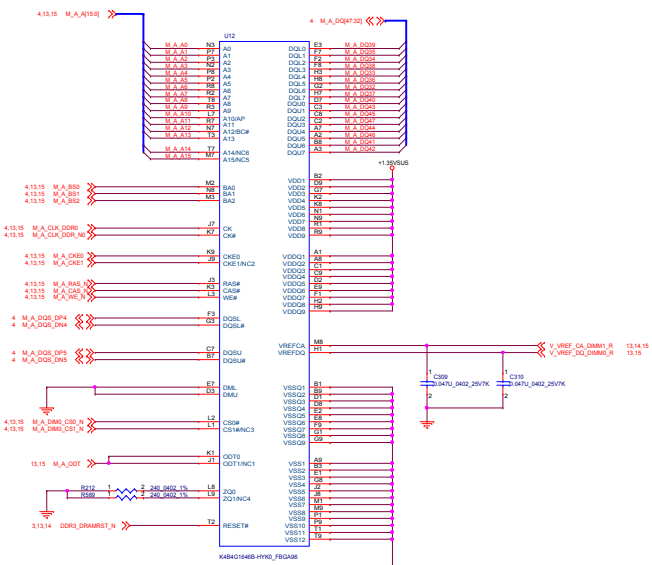
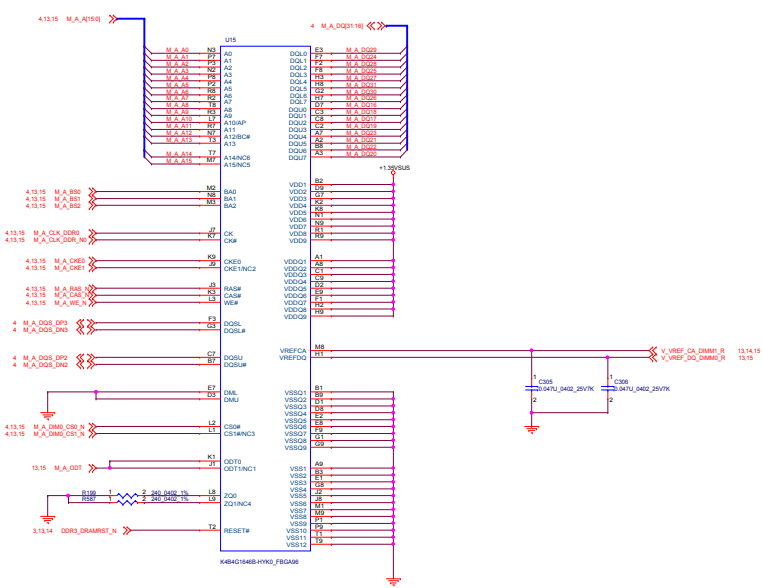
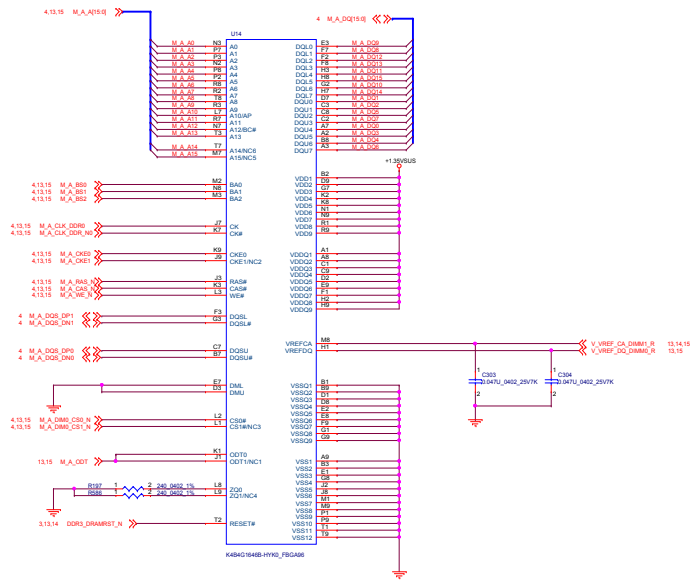
A

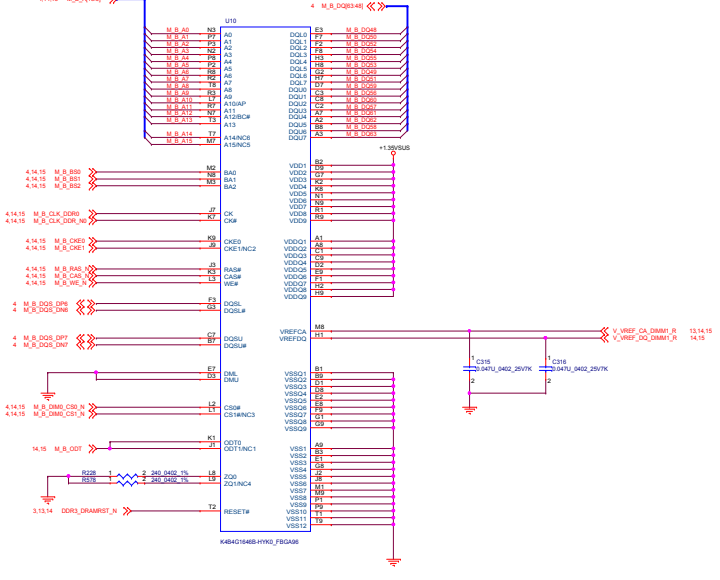
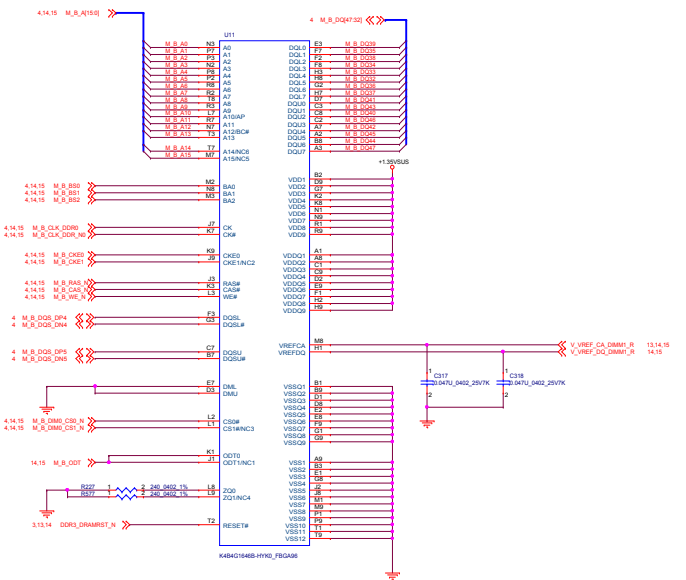
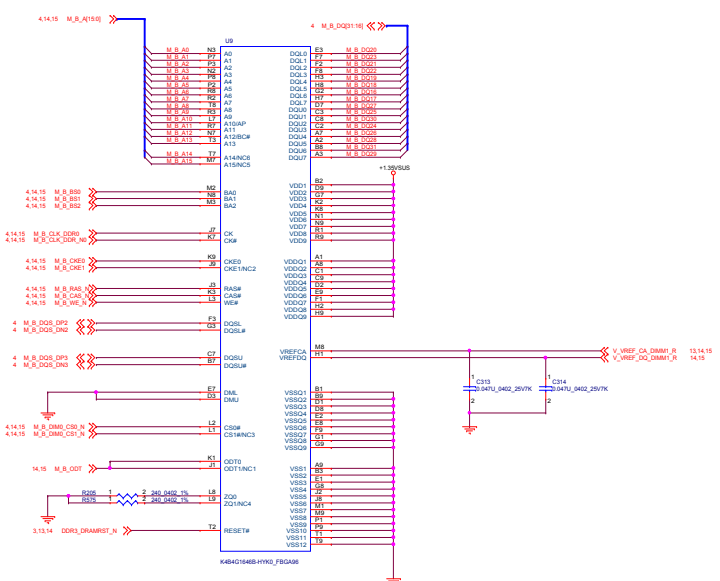
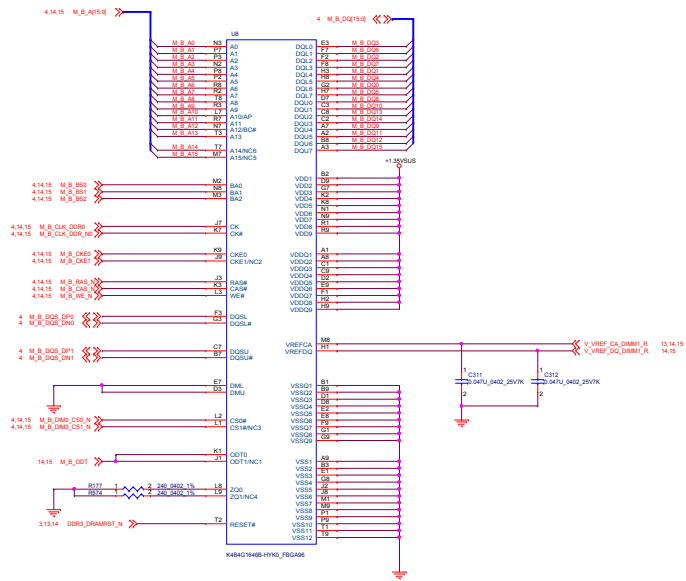
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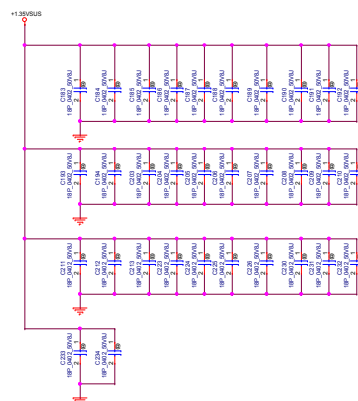
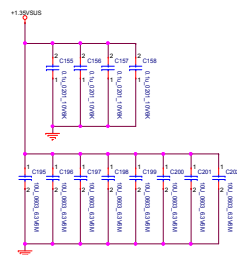
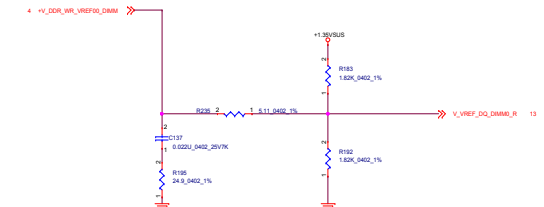
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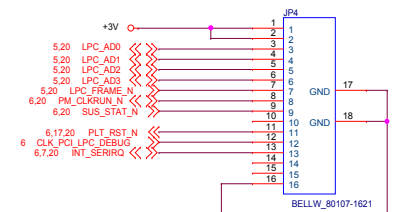
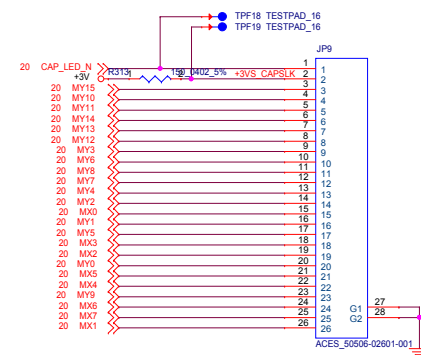
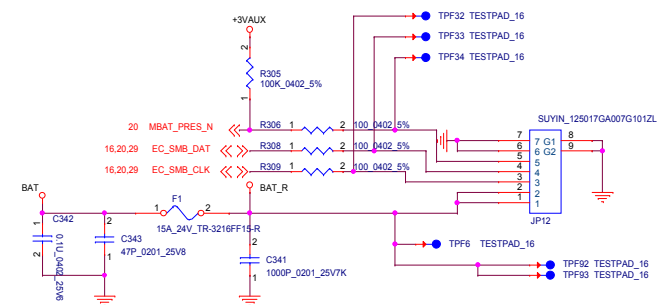
B

A



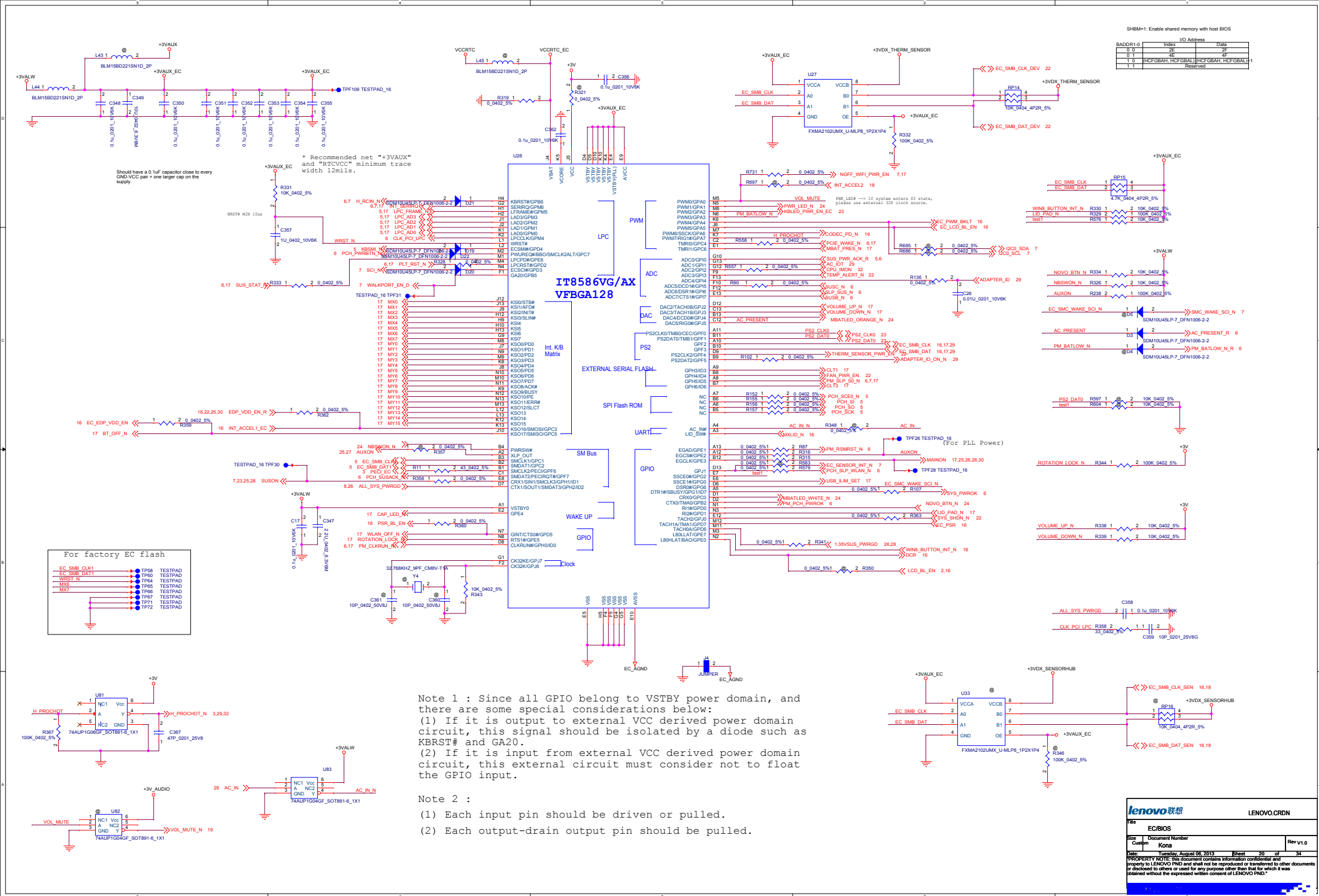


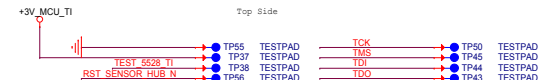
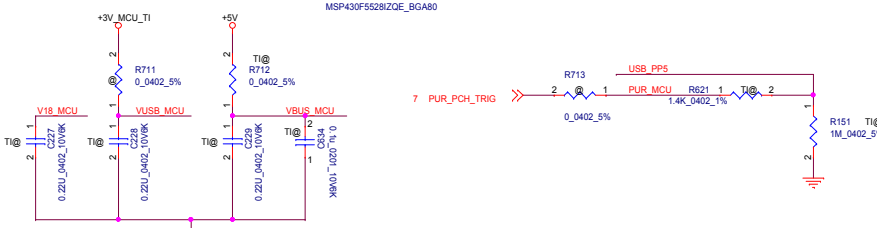
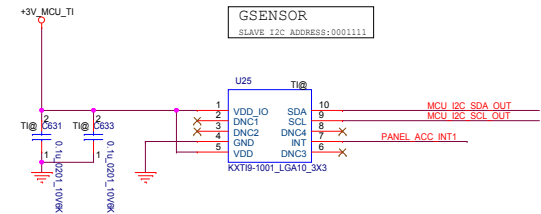
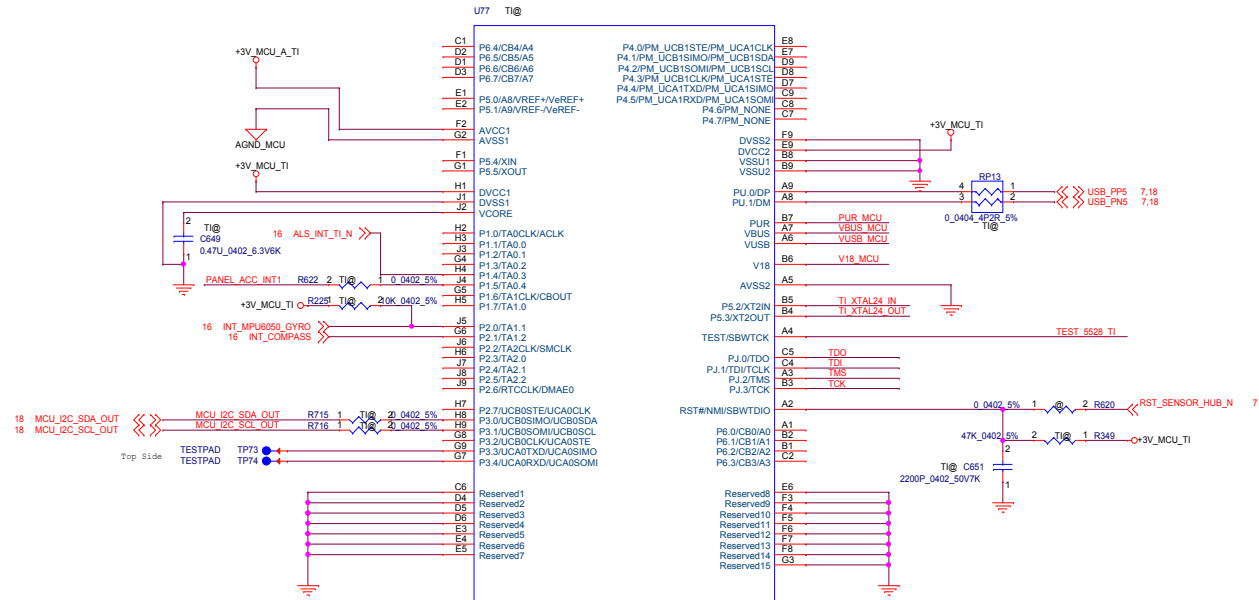
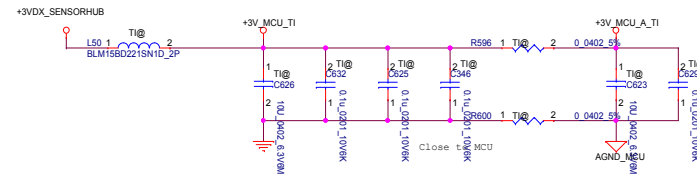
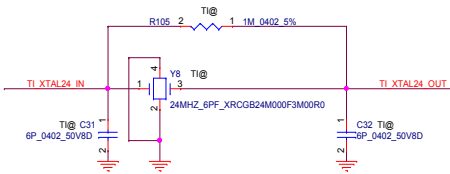




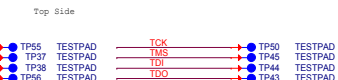
5 SATA_RXP1 <<< C276 2 | 1 0.01U 0201 10V8K >>> SATA_RXP1_C 17
5 SATA_RXN1 <<< C277 2 | 1 0.01U 0201 10V8K >>> SATA_RXN1_C 17

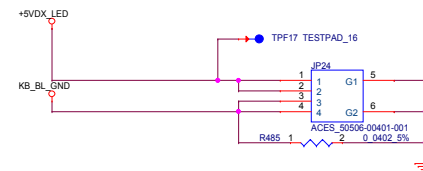
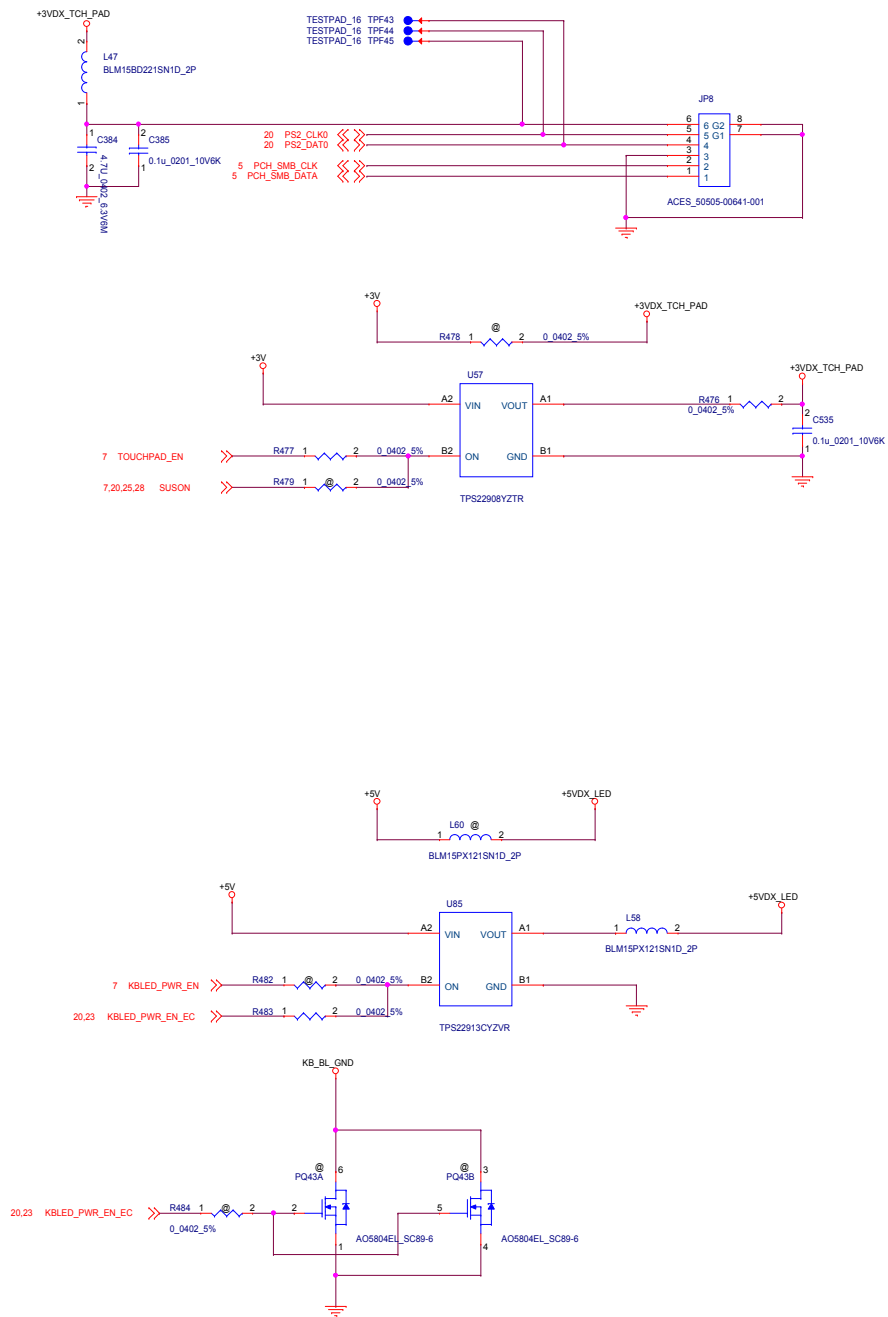
5 SATA_TXN1 <<< C272 2 | 1 0.01U 0201 10V8K >>> SATA_TXN1_C 17
5 SATA_TXP1 <<< C273 2 | 1 0.01U 0201 10V8K >>> SATA_TXP1_C 17

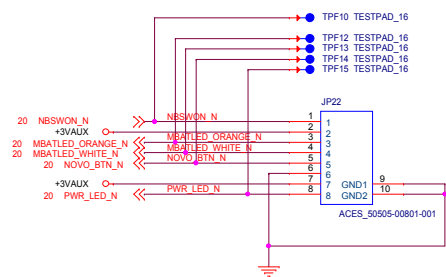


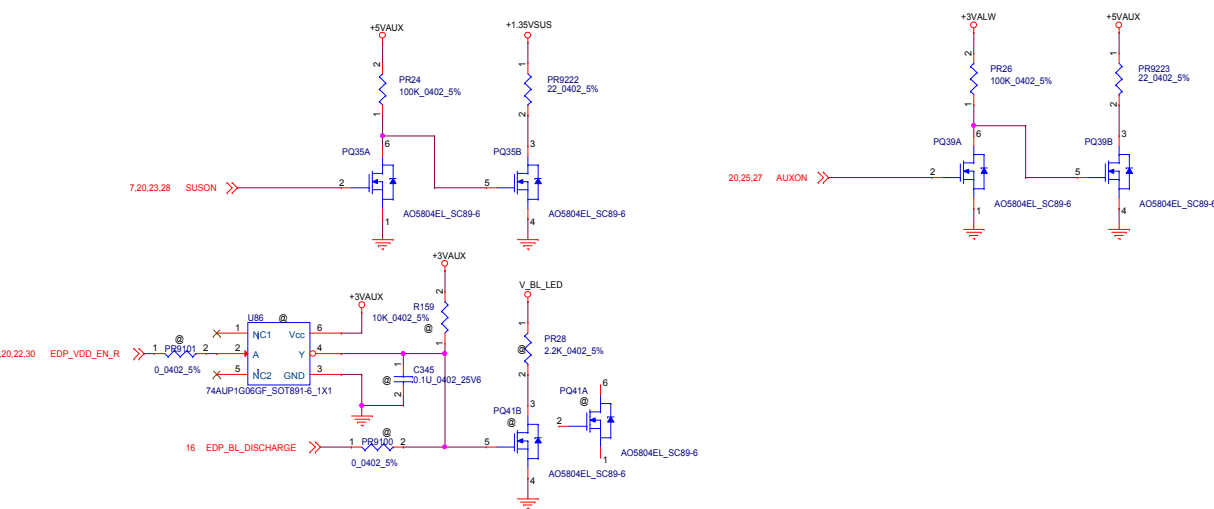
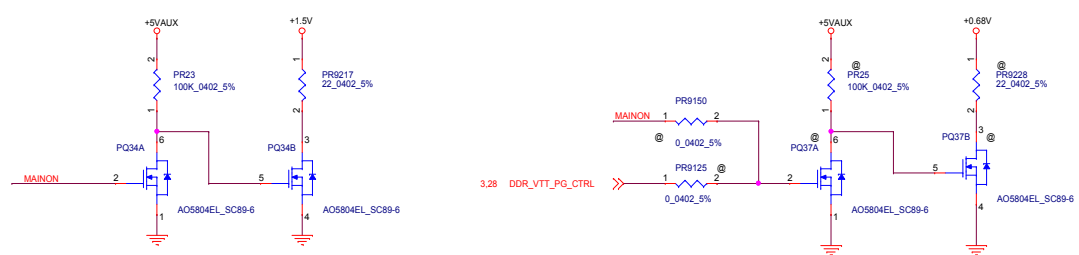
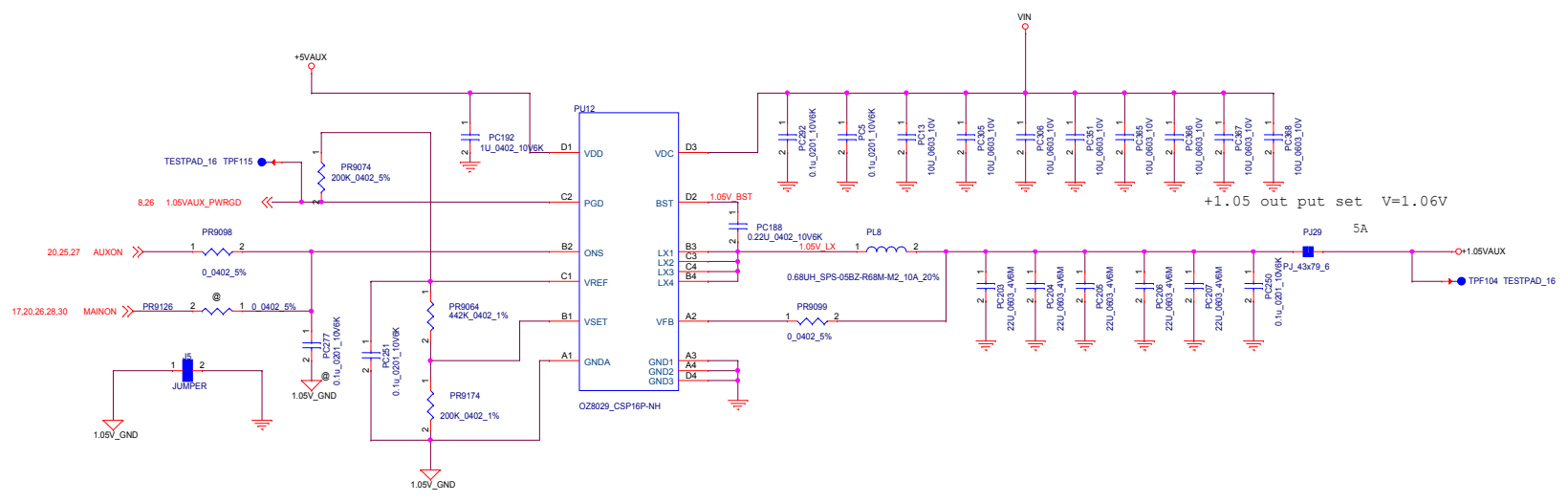


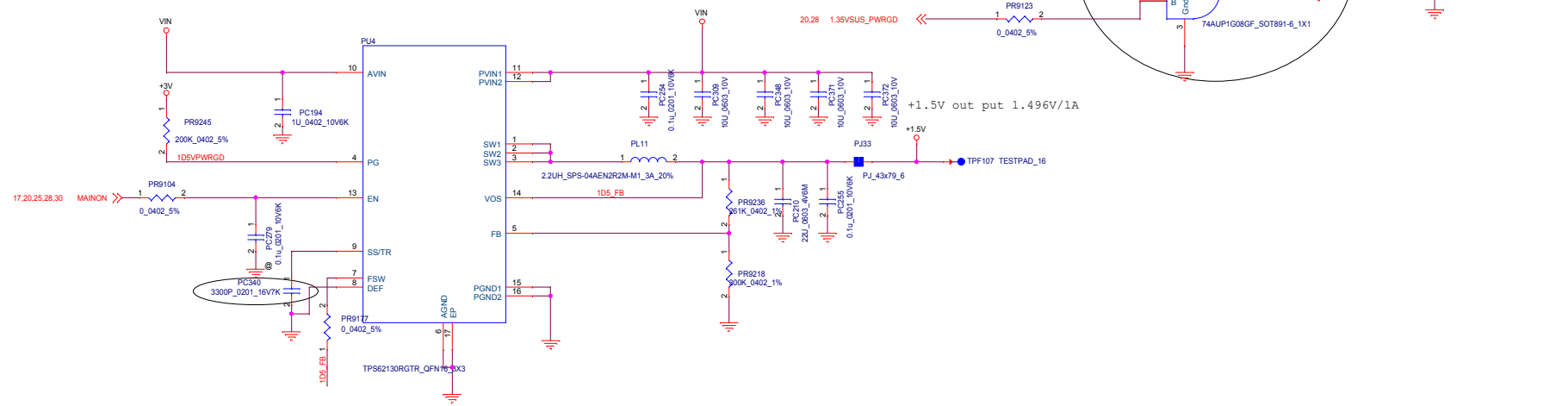
MCU JTAG

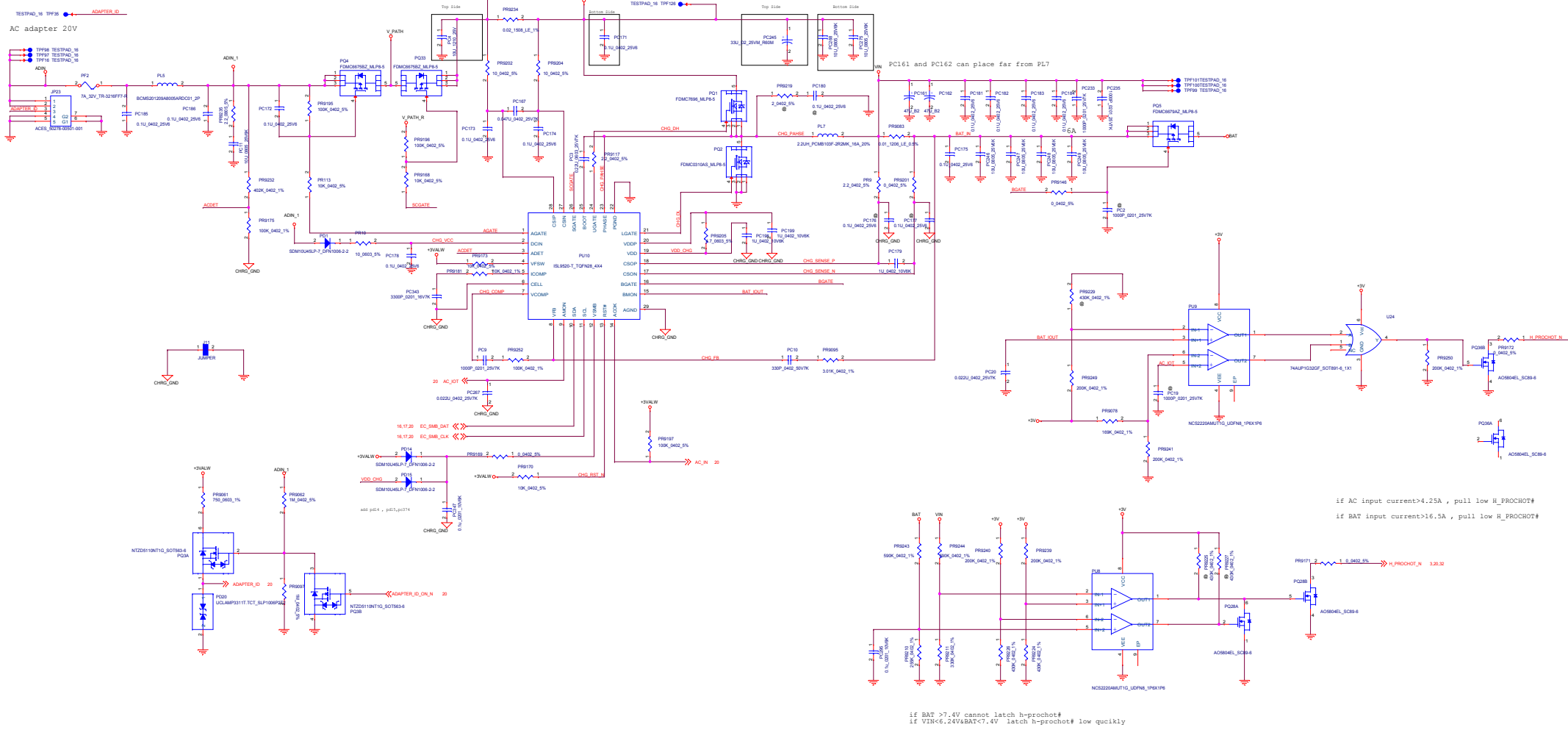




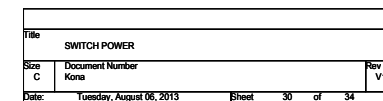


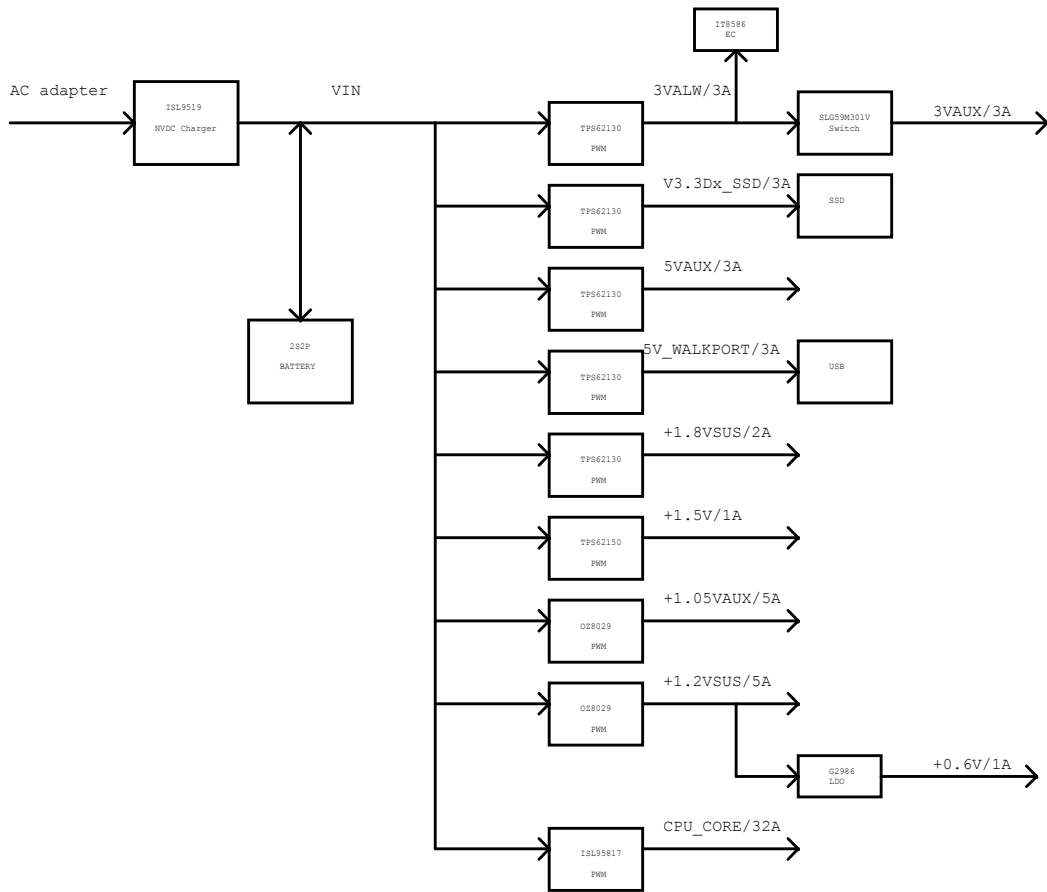






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	VCCRTC	VIN	+3VALW	+3VAUX +5VAUX +1.8VAUX	+1.35VSUS +0.68VSUS	+3V/+5V +1.0V/+1.05V +1.05_VCCST CDS3_VPE CPU_CORE
S0	V	V	V	V	V	V
S3	V	V	V	V	V	X
S4/S5 (AC)	V	V	V	V	X	X
S4/S5 (DC)	V	V	V	X	X	X
G3	V	X	X	X	X	X

	SUSC# (SLP_S4)	SUSB# (SLP_S3)	+VALW	+VAUX	+VSUS	+V
S0	V	V	V	V	V	V
S3	V	X	V	V	V	X
S4/S5 (AC)	X	X	V	V	X	X
S4/S5 (DC)	X	X	V	X	X	X
G3	X	X	X	X	X	X

TESTPAD_16 TPF47
TESTPAD_16 TPF46
TESTPAD_16 TPF48
TESTPAD_16 TPF50
TESTPAD_16 TPF49
TESTPAD_16 TPF51
TESTPAD_16 TPF53
TESTPAD_16 TPF52
TESTPAD_16 TPF54
TESTPAD_16 TPF55

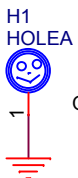
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TESTPAD_16 TPF59
TESTPAD_16 TPF61
TESTPAD_16 TPF63
TESTPAD_16 TPF62
TESTPAD_16 TPF64
TESTPAD_16 TPF65

TESTPAD_16 TPF67
TESTPAD_16 TPF66
TESTPAD_16 TPF68
TESTPAD_16 TPF70
TESTPAD_16 TPF69
TESTPAD_16 TPF71
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TESTPAD_16 TPF72
TESTPAD_16 TPF74
TESTPAD_16 TPF75

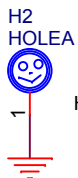
TESTPAD_16 TPF77
TESTPAD_16 TPF76
TESTPAD_16 TPF78
TESTPAD_16 TPF80
TESTPAD_16 TPF79
TESTPAD_16 TPF81
TESTPAD_16 TPF83
TESTPAD_16 TPF82
TESTPAD_16 TPF84
TESTPAD_16 TPF85

Put these 20 test point to TOP

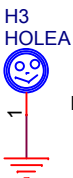
Put these 20 test point to Bottom



OVAL_Hole_22_55_SMD



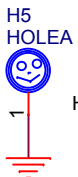
Hole_22_55_SMD_Half



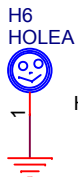
Hole_38_6_SMD



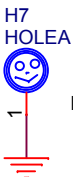
Hole_22_55_SMD



Hole_38_55_70_SMD



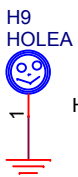
Hole_38_55_70_SMD



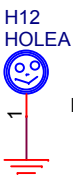
Hole_38_55_70_SMD



Hole_38_55_70_SMD




Hole_14_20_SMD



Hole_22_SMD



OVAL_Hole_14_18_SMD

联想

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Title		
Mounting Hole/EMI		
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Date: Tuesday, August 06, 2013		
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